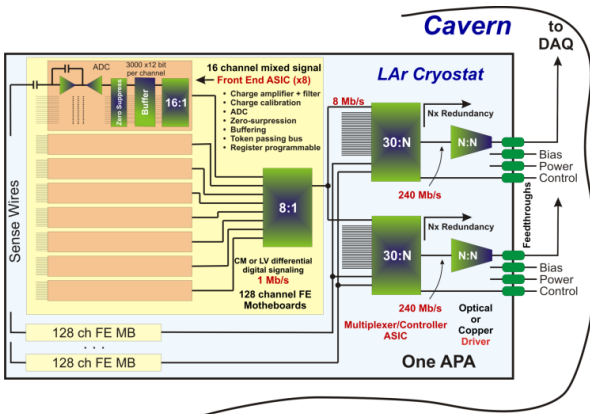54 front end boards for 3 APAs

Data out – 18 x3 twisted pair
Control – 18 x2 twisted pair
Clock – 2 twisted pair
Program – 4 twisted pair
Bias – 3 lines
Power – 6 lines

LAr 40 version